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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/686,962	10/15/2003	Peter Hazucha	42P15901	8829

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EXAMINER	
TON, MY TRANG	
ART UNIT	**PAPER NUMBER**
2816	

DATE MAILED: 09/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/686,962	HAZUCHA ET AL.	
	Examiner	Art Unit	
	My-Trang N. Ton	2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *Amdt. filed 6/23/05*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1,3,8,9,11-16,20 and 22-26* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1, 3, 8-9, 11-16, 20, 22-26* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

MY-TRANG NU TON
PRIMARY EXAMINER

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Drawings

The informal drawings filed on 10/15/03 are acceptable for examination purpose only.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 3, 8-9, 11-12, 15-16, 20, 22-24 and 26 are rejected under 35 U.S.C. 102(b) as being anticipated by Garrity et al (U. S Patent No. 5,894,284).

Garrity et al disclose in Fig. 6 a circuit including:

a switched capacitor transformer (504) comprising:

a first capacitor (614) comprising a first terminal (connected between 620 and 630) and a second terminal (connected between 632 and 622);

a first input port (VAGO) and a second input port (VAG); and

a first output port (VAGO connected to 680) and a second output port (VAG connected to 682);

a voltage reference circuit (602) to provide a reference differential voltage (+VRESIDUE, -VRESIDUE) at the first and second input port (via 620, 622);

a functional unit coupled to the first and second output ports (inherently seen as the circuit connected to VAGO (coupled to 680), VAG (coupled to 682) to receive

the reference differential voltage and to operate based upon the reference differential voltage (based on +VRESIDUE, -VRESIDUE), and

a clock generator (p2, p1) having a cycle of operation (clock phase p1, clock phase p2) and coupled to the switched capacitor transformer (504) so that the first and second terminals (connected between 620 and 630, 632 and 622) of the first capacitor (614) are coupled to the first and second input ports (VAGO,VAG), respectively, and the reference differential voltage (+VRESIDUE, -VRESIDUE) is developed between the first and second terminals of the first capacitor (614) during a first portion of the cycle of operation (when 630 and 632 ON), and

so that the first and second terminals of the first capacitor (614) are coupled to the first and second output ports, respectively, and the reference differential voltage (+VRESIDUE, -VRESIDUE) is developed between the first and second output ports during a second portion of the cycle of operation as recited in claim 1.

Regarding claim 3: the first and second portions of the cycle of operation are non-overlapping (during clock phase P2, 630 are enabled & during clock phase P1, 620 are enabled), and there is no portion of the cycle of operation for which the first terminal of the first capacitor is coupled to both the first input port and the first output port or for which the second terminal of the first capacitor is coupled to both the second input port and the second output port (see col. 6, lines 32 – 43).

Claim 7 is similarly rejected as claim 6.

Regarding claim 8:

a second capacitor (610) comprising a first terminal (connected between 620, 670) coupled to the first input port and a second terminal (connected between 622, 672) coupled to the second input port; and

a third capacitor (664) comprising a first terminal coupled to the first output port (VAGO connected to 680) and a second terminal coupled to the second output port (VAG connected to 682).

Claim 9 is similarly rejected as claim 8.

Regarding claim 11: a clock generator having a cycle of operation to switch the first and second switches ON (630 and 632 ON) during the first portion of the cycle of operation and to switch the third and fourth switches ON (620, 622 ON) during the second portion of the cycle of operation; wherein the first and second portion of the cycle of operation are disjoint (cycle phase P1, cycle phase P2 are disjoint).

Regarding claim 12: wherein for no portion of the cycle of operation does the clock generator switch the first and third switches both ON, the first and fourth switches both ON, the second and third switches both ON, or the second and fourth switches both ON (630 and 632 switches ON depends on p2; 620 and 622 switches ON depends on p1, see col. 6, lines 32-43, col. 7).

Claims 15-16 are similarly rejected as claim 8.

Claim 20 is similarly rejected as claims 11-12.

Regarding claim 22:

a voltage reference circuit (602) to develop the reference voltage (+VRESIDUE, -VRESIDUE),

a functional unit (inherently seen as the circuit coupled to VAGO (connected to 680) and VAG (connected to 682), and

a switched capacitor transformer (504) comprising a capacitor (614, 610, 664) and a plurality of switches (630, 632, 620, 622, 670, 672, 680, 682) that couple the capacitor to the voltage reference circuit (602) in order to receive the reference voltage (+VRESIDUE, -VRESIDUE) during a first period of a cycle of operation (phase p2, 670 close) and that couple the capacitor to the functional unit in order to deliver the reference voltage to the functional unit during a second period of the cycle of operation (phase p1).

Claim 23 is similarly rejected as above claims: a switched capacitor circuit (504), a first and second input port (VAGO, VAG), a first and second output port (VGAO (coupled to 680), VAG (coupled to 682), a capacitor (614), and a plurality of switches (630, 632, 620, 622, 670, 672, 680, 682), a voltage reference circuit (602), a function unit (seen as circuit connected to VAGO (coupled to 680) and VAG (connected to 682)).

Claim 24 is similarly rejected as claim 11.

Claim 26 is similarly rejected as claim 8.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 13-14 and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Garrity et al as applied to claim 10 above.

As stated above, every element of the claimed invention recited in above claims can be seen in the circuit of Garrity. However, this reference does not specifically show the "pMOSFETs, nMOSFETs" (claims 13 and 25).

However, field effect transistors are well-known switching devices and patentable equivalent to switches since no unobvious results are seen produce from there use. Therefore, it would have been obvious at the time of the invention was made for one skilled in the art to utilize these particular types of transistors (MOSFETs) because of their well-known advantages in performance and integration. MOSFETs have very short switching times and very low electrical power consumption. Furthermore, regarding "first and third switches are pMOSFETs" and "second and fourth switches are nMOSFETs", it would have been obvious at the time of the invention was made for one skilled in the art to utilize first and third switches (630, 620: seen as pull up switches) are pMOSFETs and second and fourth switches (632, 622: seen as pull down switches) are nMOSFETs so that these transistors operate in saturation mode rather than follower mode.

Claim 14 is similarly rejected as claim 8.

Response to Arguments

Applicant's arguments filed 6/23/05 have been fully considered but they are not persuasive. Examiner has thoroughly reviewed Applicant's arguments but firmly

believes that the cited reference reasonably and properly meet the claimed limitation as rejected.

Applicant's argument – Garrity does not teach **a voltage reference circuit to provide reference differential voltage**, via switched capacitor transformer, to one or more functional units **to receive the reference differential voltage** and to operate based upon the reference differential voltage.

Examiner's response - Garrity clearly shows in Fig. 6 **a voltage reference circuit (602) to provide reference differential voltage (+VRESIDUE, -VRESIDUE),** via switched capacitor transformer (504), to functional units (seen as the circuit connected to VAGO (coupled to 680) and VAG (coupled to 682)) **to receive the reference differential voltage (+VRESIDUE, -VRESIDUE)** and to operate based upon the reference differential voltage (+VRESIDUE, -VRESIDUE).

Therefore, these claims are not seen to distinguish the present invention over the prior art.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

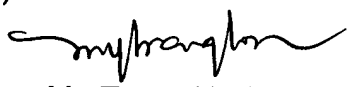
A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



My-Trang N. Ton
Primary Examiner
Art Unit 2816

August 30, 2005